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Attorney Docket No.: 000939-052170US

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By: Juma.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Harold S. Crafts

Application No.:

Filed:

For: SEA-OF-CELLS ARRAY OF

TRANSISTORS

Examiner:

Art Unit:

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR §1.97 and §1.98

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

The references cited below and on attached forms PTO/SB/08A and PTO/SB/08Bare being called to the attention of the Examiner. Copies of the references are not enclosed. Copies of the references were provided in U.S. Patent Application No. 09/704,115, and/or U.S. Patent Application No. 08/837,589, upon which this application relies for an earlier effective filing date. Thus, pursuant to 37 CFR § 1.98(d), providing copies of the references is not necessary. It is respectfully requested that the cited references be expressly considered during the prosecution of this application.

The cited references are:

Harold S. Crafts Application No.: Page 2

> 1) U.S. Pat. 6,269,466 Inventor: Crafts, H.S. Issue date: July 31, 2001

2) U.S. Pat. 5,869,900 Inventor: Crafts, H.S. Issue date: February 9, 1999

3) U.S. Pat. 5,671,397 Inventor: Crafts, H.S. Issue date: September 23, 1997

4) U.S. Application No. 09/703,948 Inventor: Crafts, H.S. Filing date: October 31, 2000

5) U.S. Application No. 09/703,767 Inventor: Crafts, H.S. Filing date: November 1, 2000

As provided for by 37 CFR 1.97(g) and (h), no inference should be made that the information and references cited are prior art merely because they are in this statement and no representation is being made that a search has been conducted or that this statement encompasses all the possible relevant information.

Applicant believes that <u>no fee is required</u> for submission of this statement. However, if a fee is required, the Commissioner is authorized to deduct such fee from the undersigned's Deposit Account No. 20-1430. Please deduct any additional fees from, or credit any overpayment to, the above-noted Deposit Account.

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Respectfully submitted,

Irvin E. Branch Reg. No. 42,358

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Tel: 303-571-4000 Fax: 303-571-4321

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Substitute	for form 1449/PTO			Complete if Known				
				Application Number				
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STAT	TEMENT B	Y A	PPLICANT	First Named Inventor	Crafts, Harold S.			
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Document Number			- Bublication Data	Name of Patentine C	Pages Columns Lines Miles			
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Examiner Signature Date Considered

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Examiner	Cite No.1	Document Number Number Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear			
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Examiner Initials *	Cite No.1	item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	٦
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-	СВ	An Efficient Algorithm for some Mltirow Layout Problems; J. FELDMAN et al.; IEEE Transactions on Computer-Aided Design of Integrated Circuits & Systems; Vol. 12, No. 8, August 1993; pp. 1178-1185	
	AC	10K-Gate GaAs JFET Sea of Gates; H. KAWASAKI et al.; IEEE Journal of Solid-State Circuits; vol. 26, No. 10; Otober 1991; pp. 1367-1370	
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	AE	A Design-System for ASIC's with Macrocells; B. KORTE et al.; IEEE, Euro ASIC '90; 1990; PP. 220-224	
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